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### 1 Schematic driven module generation for analog circuits with performance optimization and matching considerations

Naiknaware, R.; Fiez, T.;

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16 **On the integration of partitioning and global routing for rectilinear placement problems**

*Chingwei Yeh; Chi-Shong Wang;*

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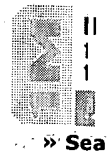
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